

Number of seats vacant as on 19.07.2026 at 10 AM:

Note:

- 1) This seat matrix will be used for the last round of counselling for GATE-qualified candidates to be held on 20.07.2026 (Monday). An updated vacant seat matrix will be published on the evening of 20.07.2026 for the last round of counselling for non-GATE candidates to be held on 21.07.2026 (Tuesday)
- 2) All vacancies created due to withdrawal of candidates till 10 AM of 19.07.2026 have been incorporated in the tables given below. If any more candidate withdraws admission by 10 AM of 20.07.2026, then the vacant seat matrix will be updated accordingly before the beginning of counselling on 20.07.2026.
- 3) Some seats have been marked with an asterisk (*) in the tables given below. No GATE-qualified candidate was found for these seats in the previous rounds. Therefore, supernumerary seats were created corresponding to these seats and filled with non-GATE candidates in the preceding round as per the provisions given in the information brochure. These seats may now be filled with only GATE-qualified candidates on 20.07.2026 and will not be offered to non-GATE candidates on 21.07.2026.
- 4) If there is no candidate seeking admission to the reserved seats, then those seats will be converted to General seats on 20.07.2026, as per the provisions given in the information brochure, and made available to all candidates irrespective of their category.

Category	Department	ECE	ECE	ECE	CSE	CSE	CSE	IT	EE	ICE	ME	ME	BSE
	M.Tech. Programme	SP	ES	QT	CS	CBR	ADS	IT	PE	IN	ME	CAD	BI
General	General	9	3+1*	3+1*	3+1*	1	1+1*	2	9+1*	7	8+1*	5	2
	PwD	1	1	0	0	1	1	1	0	0	0	0	0
SC	SC	4	2	6	0	1	0	0	4	3	4	2	3
	PwD	0	0	0	0	0	0	0	0	1	2	0	0
ST	ST	2	2	4	1	2	2	2	2	1	2	1	1
	PwD	0	0	0	0	0	0	0	1	0	0	0	0
OBC	OBC	8	0	8	2	0	0	0	7	4	8	5	4
	PwD	0	1	0	1	1	1	0	0	0	2	0	0
EWS	EWS	4	1	2	1	1	0	1	3	2	2	2	2
	PwD	0	0	0	0	0	0	1	0	0	0	0	0

Departments: ECE = Electronics and Communication Engineering, CSE = Computer Science and Engineering, IT = Information Technology, EE = Electrical Engineering, ICE = Instrumentation and Control Engineering, ME = Mechanical Engineering, BSE = Biological Science and Engineering.

M.Tech. Programmes: SP = Signal Processing, ES = Embedded System and VLSI, QT = Quantum Technology, CS = Computer Science and Engineering, CBR= Cyber Security, ADS= Artificial Intelligence and Data Science, IT = Information Technology, PE = Power and Energy Systems, IN = Intelligent System, ME = Mechanical Engineering, CAD = CAD/CAM, BI = Bioinformatics.

Number of seats vacant as on 19.07.2026 at 10 AM:

Category	Campus	East	East	East	East	West
	Department	ECE (E)	ECE (E)	CSE (E)	CSE (E)	Civil (W)
	M.Tech. Programme	VLSI	DC	IS	AI	STR
General	General	2+2*	12	0+1*	3	10
	PwD	0	1	2	0	2
SC	SC	4	4	4	1	4
	PwD	2	0	0	0	0
ST	ST	2	2	2	4	2
	PwD	0	0	0	0	0
OBC	OBC	6	8	7	0	8
	PwD	2	0	0	0	0
EWS	EWS	2	3	0	1	4
	PwD	0	0	0	0	0

Departments: ECE = Electronics and Communication Engineering (East Campus), CSE = Computer Science and Engineering (East Campus), Civil (W) = Civil Engineering (West Campus).

M.Tech. Programmes: VLSI = VLSI, DC = Digital Communications, IS = Information Security, AI = Artificial Intelligence, STR = Structural Engineering.