Indian Institute of Information Technology
Design and Manufacturing Kurnool

Information Brochure

Ph.D. Admissions

June 2026 Session

Department of

Electronics and Communication Engineering

About the Electronics and Communication Engineering department:

The department of Electronics and Communication Engineering is pioneering in engineering education by incorporating the fields like Design and Manufacturing concepts as a unique in its nature. The Institute promotes the multidisciplinary teaching of engineering courses by incorporating the subjects like Product Design and practice, Design, Manufacturing and Prototyping, Projects along with internships in all the programs of the department.

For more detail, visit dept. page: https://iiitk.ac.in/Electronics-and-Communication-Engineering/page

Eligibility Criteria for Full-Time Ph.D.:

Applicants holding Master's degree:

- Minimum Education Qualifications: Applicants with a postgraduate degree (M. Tech. /M.E. /M.S. (Research) or equivalent) in a relevant branch/specialization from any institute with a CGPA of 6.5/10 or 60% for UR/OBC/EWS category and CGPA of 6.0/10 or 55 % for SC/ST/PwD category.
- Screening and Selection: Entrance Test* and/or Interview. (* based on the number of applications received)

Direct admission to PhD with Graduation (B.E. / B. Tech.):

Minimum Education Qualifications:

- Applicants with a Bachelor's degree (B. Tech. / B.E. or equivalent) in a relevant branch/specialization from any CFTI with a CGPA of 7.5/10 or 70% for UR/OBC/EWS category and CGPA of 7.0/10 or 65 % for SC/ST/PwD category. (**GATE Qualification is not Mandatory**)

- Applicants with a Bachelor's degree (B. Tech. / B.E. or equivalent) in a relevant branch/specialization from any **non-CFTI** with a CGPA of 7.5/10 or 70% for UR/OBC/EWS category and CGPA of 7.0/10 or 65 % for SC/ST/PwD category. (**GATE Qualification is Mandatory**)
- Screening and Selection: Entrance Test* and/or Interview. (*based on the number of applications received)

- In cases where the candidates are directly admitted to the PhD programme in Engineering /Sciences with a Bachelor's Degree in Engineering/Technology, the scholar should successfully complete prescribed courses (Coursework) with a minimum of 24 Credits.

Eligibility Criteria for Part-Time Ph.D.:

Educational Qualifications:

Master's/M.S. (by Research) degree in the appropriate branch of study with first class and a minimum 60% aggregate marks or CGPA ≥ 6.5 (out of 10) in UG and PG.

Note: For engineering departments, candidates with B.Tech. /B.E. degree may also be considered, if the candidates have at least 6 years of experience with proven track record of research experience.

Essential experience: (Candidates should satisfy any one of the below-mentioned criteria)

Permanent employees who can submit "No Objection Certificate" (NOC) from their employer and are working in the cadre equivalent to Scientist-C/Assistant Professor/Lecturer in Government R&D laboratories /Government organizations / Government industries/ PSUs / State Govt. Undertaking with at least three years of experience are eligible.

(OR)

Permanent/ Regular Employees from Private organization /Industries/Education Institutions with R & D facilities (i.e., established at least five years before the last date of applying for PhD (Part-time) admission as per the advertisement) with membership in CII/ ASSOCHEM or any other equivalent membership having at least three years of experience are eligible.

(OR)

Permanent employees of IIITDM Kurnool, having at least 3 years of experience.

Highlights:

1. Scholars under the HTRA category are eligible for Half-Time Research Assistantship (HTRA) provided by the Institute

   1st and 2nd Year (JRF): 37,000/- & 3rd to 5th Year (SRF*): 42,000/-

2. Financial assistance and HRA are provided to selected Full time scholars based on the prescribed rules of MoE and Institute rules respectively.
3. Financial support for Rs. 2 Lakhs for attending National/International conferences during the PhD period for regular PhD Scholars (MoE fellowship).
4. Opportunity for a Research Associate position if the thesis submitted at least 6 months early

Screening and Selection:

a) Respective Departments will conduct the shortlisting and selection process.

b) Eligible candidates possessing the minimum educational qualifications and satisfying additional criteria set by the institute from time to time only will be called for the written test and/or Interview.

c) An entrance examination shall be conducted for all the applicants (Full-Time and Part-Time) who do not have GATE or NET qualification.

d) The syllabus for the written test and/ or Interview is **GATE 2026 syllabus in respective subject.**

e) Applicants with a valid GATE or CSIR/UGC-NET qualification will be directly eligible for the interview.

f) The question paper for written exam will consist of 50 MCQ based on the GATE/NET syllabus.

g) Each question will carry 2 marks, with a negative marking of 0.5 marks for every incorrect answer.

h) The cut-off marks shall be determined in line with the GATE 2026 standards.

i) The exam will be conducted either in CBT (Moodle) mode or offline based on number of applications.

j) Based on the academic record and the performance of the candidates in the written test and/or Interview test, the selection committee will finalize the applicants for admission to the Ph.D. programme.

Specializations:

Name of the Department	Ph.D. Category	Broad Research Areas
	HTRA (Full-Time)	VLSI Design, In-memory Computing, SNNs DNNs Hardware, Microelectronics, Embedded and IoT Systems, Cryptography Hardware Architectures, Embedded ML, AI, Deep learning, Sensor Design with MEMS, Computer architecture, DSP, Image processing, Analog VLSI, Mixed Signal IC Design, Formal Hardware Verification, VLSI, Embedded Systems, Cyber

ECE	Part - Time	Physical Systems, Cryptography, IoT, Software Defined Radio, Biomedical Instrumentation, Electronic system design, Electronic materials, Biosensor, Biodegrable/Sustaiable for Electronics, Drone Data Processing, Industrial Automation Wireless Communication, 5G , Drones, Robotics, Visible light communication, Quantum communication, 6G communication, Navigation, Electromagnetics, Signal & Power integrity, Inverse Problems, RF and Microwave, Microelectronics, Semiconductor Device Modeling, Analog and digital IC design, Microelectronics, Optoelectronic Devices, Opto Semiconductor Device Modeling, Digital Signal Processing, Power System Instrumentation, Power quality, FPGA Based System Design, Applied electronics, Speech & audio processing, Interface electronics, Electronic digitizers, Analog circuits, Analog and digital instrumentation, Communications and allied areas, Smart Sensing with IoT, Smart Sensing using Drones, Smart Sensing using Wireless Networks, 6G Networks, Cybersecurity, Network Security, Explainable AI, Edge AI, AI/ML in Communication/Networking, UAV-assisted wireless networks, Small-world networks, Social Networks, UAV path planning, Hybrid communication systems, Machine-type communications

For Ph.D. admissions under Sponsored Project:

PI/Supervisor details	**Title of the project**	**Broad Research Areas**
Dr. Mohamed Asan Basiri	C2S project	Computer architecture, DSP, Image, processing, Analog VLSI, Mixed Signal IC Design, Formal Hardware Verification, VLSI, Embedded Systems, Cyber Physical Systems, Cryptography, IoT, Software Defined Radio

Infrastructure and Research Facilities:

The Department has established the following laboratories to meet all the requirements of the programs being offered. Every course is being taught in the classrooms and laboratory with concept-based experimentation along with theoretical concepts.

- VLSI and Digital Signal Processing Lab
- Embedded Systems -IoT Lab
- Electrical Drivers and Instrumentation Lab
- Drones Lab- Institutional Project
- 5G Use case Laboratory- Institutional Project
- Microprocessor and Microcontroller Lab
- Antenna and Microwave Lab
- RF and Simulation Lab
- Communications Lab
- Analog Electronics Lab
- Digital Electronics Lab
- Electronic Manufacturing Lab

Convener/coordinator of Ph.D. admissions and contact details:

- Dr. S Subba Rao, Assistant Professor ; 08518289-180 / ssr29@iiitk.ac.in

Online application portal link: https://iiitkadm.samarth.edu.in/

Scan for Online Application:

Bank Account Details and Payment QR Code:

Account Name: **IIIT DESIGN AND MANUFACTURING**
Account Number: **37806955974**
IFSC Code: **SBIN0064655**
Branch: IIITDM KURNOOL CAMPUS.

Important Dates:

Web notification of the Ph.D. Advertisement	8th April 2026
Online application registration process start date	13th April 2026
Last date for the submission of online Application form	17th May 2026
Notification of shortlisted candidates for Interview/ Written Test	20th May 2026